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(54) Thermal housing for optical circuits

(57) A housing (11) for optical integrated circuits for protecting the circuit (48) from temperature induced stresses has a base (12) and cap (13) and a thermal bed (44) contained therein. The thermal bed has a region for containing the integrated circuit therein, suspended in a thermally conductive grease (47). At least one heater (54) is affixed to the thermal bed for heating it, and temperature monitoring means supplying signals to a control means (15) for activating the heater means when the temperature of the thermal bed falls outside of the operating temperature range.

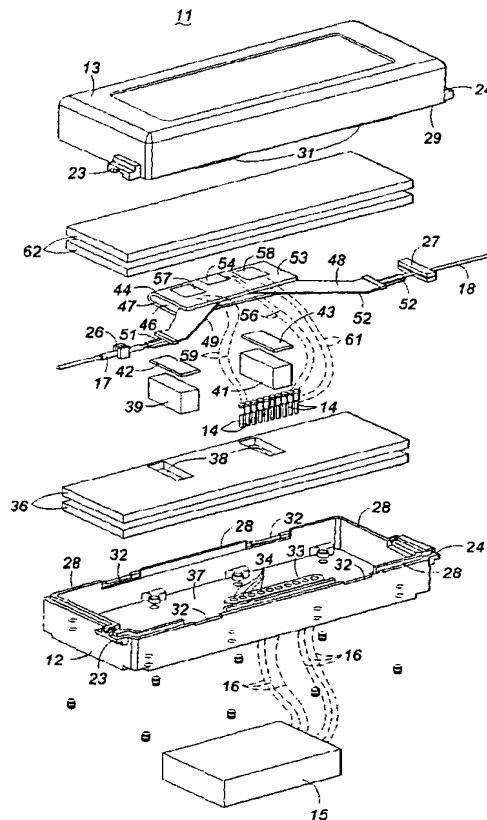


FIG. 3

Description

Field Of Invention

[0001] This invention relates to housings for optical integrated circuits and, more particularly, to a thermal housing having temperature control for maintaining an optical integrated circuit within a fixed temperature range.

Background Of The Invention

[0002] Communication systems, as well as other signal processing arrangements, are, more and more, being created utilizing optical signals requiring the use of optical fibers and the numerous components associated therewith, or are being converted to such use. The tremendous bandwidths characterizing optical signal transmission make the use of such systems the preferred mode for signal transmission. However, despite the obvious advantages from the use of optical signals, such transmission gives rise to problems that are unique to light wave signal manipulation, problems which generally are not present in lower frequency signal transmission utilizing conductive wires. For example, optical switching, multiplexing, and demultiplexing are all operations, along with others, that present problems. Such operations have, in general, required a concatenation of discrete optical components, and, as a result, suffer from increased bulkiness and reduced system reliability. As a consequence, much effort has been directed at reducing the number of components by combining their operations on a single, monolithic chip which generally comprises a thin film, compact planar optical circuit. Properly designed chips can greatly improve circuit or signal transmission performance while, at the same time, insuring greatly increased reliability and an advantageous reduction in the number of discrete components in the circuit.

[0003] One example of such a monolithic device is the dense wave division multiplexer (DWDM) of the type shown, for example, in U.S. patent 5,136,671 of Dragone, the disclosure of which is herein incorporated by reference. Such a device is useful where a large number of transmission channels must be crowded into a narrow bandwidth window. Such narrow windows can result, for example, from the use of erbium doped optical amplifiers, which are widely used today, but which can severely limit the usable bandwidth. In order to accommodate many channels in the narrow window, they must be closely spaced in wavelength, such as, for example, successive wavelengths differing by 0.8 nm or 1.6 nm. By use of a new technology referred to as OASIC (optical application specific integrated circuits), thin film planar optical circuits can be formed to produce such a DWDM as discussed, on a single wafer or chip. Such a chip generally comprises a thin silicon wafer upon which a low refractive index silica glass lower cladding is de-

posited. A high index core layer is then deposited, patterned, and etched to form the optical waveguides, and then an upper cladding is deposited. Wafers for a variety of functions can be produced using the OASIC technology, however, the remainder of the discussion will be directed to the DWDM in the interest of simplicity and consistency. It should be understood that these other types of integrated circuits are by no means intended to be excluded.

[0004] One of the problems arising from the use of some OASIC devices, particularly the arrayed waveguide gratings in a DWDM, is their sensitivity to temperature changes, and to physical stresses which impair their reliability. For example, in the DWDM, because the operating wavelengths of the several individual channels differ by such a small degree, any expansion or contraction or bending due to temperature fluctuations will degrade the optical performance and, in the extreme, cause circuit failure. Likewise, temperature fluctuations less than 1°C may cause degradation or failure. It has been found that degradation or failure can generally be prevented and reliability of the circuit insured if the temperature of the device is maintained at a predetermined temperature in a range of 75°C to 90°C. This maintenance temperature, specific to the individual circuit, must be controlled to within a few degrees Celsius even though the ambient temperature may vary from, for example, 0°C to 70°C. Thus, some sort of protective housing must be provided for the wafer, i.e., circuit.

[0005] Maintaining various types of electronic devices at an even temperature by housing them in sealed containers is well known in the art. In U.S. patent 4,968,121 of Brusselbach *et al.* there is shown such a housing for a crystal which comprises an oven having walls of thermally conductive material and an inner chamber within the oven which contains the crystal and maintains it in thermal contact with the walls of the inner chamber. Means are provided for heating both the oven walls and the crystal. Such an arrangement is both complicated and bulky, and, apparently, unsuitable for maintaining an IC wafer at a constant temperature with an economy of components and structure.

[0006] In any housing for maintaining a substantially constant temperature in at least a portion thereof, each component within the housing, as well as the housing itself, is subject to deleterious temperature effects during operation. Thus, expansion or contraction of the housing itself due to temperature changes can affect at least some of the components within the housing, to the detriment of the overall operation.

Summary Of The Invention

[0007] The present invention is a housing and components for an optical integrated circuit, such as a DWDM, which is structured to maintain, during operation, the temperature of a circuit and surrounding ele-

ments within a narrow temperature range, regardless of ambient temperature, for extended periods, even for years.

[0008] The housing comprises a base and a snap on cover made of a glass filled polyphenylene sulfide material having a relatively low thermal coefficient of expansion. Within the housing is a layer of polyimide fibrous material which is chosen for its relative immunity to temperature changes and for its durability and insulating qualities. A pair of support members of the same material, but hardened, support a thermal bed, which comprises a substantially U-shaped aluminum member, the legs of which define a slot for receiving the circuit chip or wafer, such as the DWDM discussed hereinbefore. The slot is filled with a thermally conductive grease which suspends the wafer and allows it to float within the slot, substantially completely covered by the legs of the U-shaped bed. Thus, the wafer is in a stress free position in the slot. On the top surface of the one leg or on the surface of both legs of the U-shaped bed is a heater which, for example, may be wrapped around the bend of the U-shape, and a pair of resistive temperature devices for monitoring the temperature of the bed. Leads from the heater and the monitoring devices pass through electrical lead through pins to the exterior of the housing, and leads are extended to a control unit which measures the temperature of the thermal bed by means of signals from the resistive temperature devices, and applies a voltage to the heater when necessary. Thus, the wafer, *i.e.*, the DWDM circuit, is contained within a uniform temperature environment and is relatively immune to mechanical stresses. For example, when the thermal bed is initially heated to the operating temperature, the wafer, because it floats within the thermal bed, is not subjected to stress. The thermal grease increases the thermal conductivity between the thermal bed and the circuit and insures that the temperature is uniform over the entire circuit and that there are no hot spots.

[0009] As will be discussed more fully hereinafter, at least some of the optical fibers are contained in ribbons, and where they join the DWDM can also be subjected to stresses. The method of assembly of the housing and components is such that this stress is substantially eliminated. After the thermal bed is placed upon the support blocks and the electrical leads connected to the feed through pins, a second layer of thermal insulation is placed within the housing to overlie the components contained therein, and the lid of the housing is snapped into place on the base.

[0010] In an alternative embodiment of the housing assembly, the temperature control circuit is contained within the housing, although power for the heater is still supplied by an external source.

[0011] The housing of the invention thus insures that the temperature of the optical circuit, *e.g.*, the DWDM, is maintained within a narrow range which is uniform over the entire area of the circuit, thereby insuring a constant, reliable performance over a number of years, *e.*

g., twenty years.

[0012] The numerous features and advantages of the present invention will be more readily apparent from the following detailed description, read in conjunction with the accompanying drawings.

Description Of The Drawings

[0013]

Fig. 1 is a plan view of the housing of the invention with associated circuitry;

Fig. 2 is an elevation view of the arrangement of Fig. 1;

Fig. 3 is an exploded perspective view of the housing of the present invention and the components contained therein;

Fig. 4 is an exploded perspective view of the housing in a second embodiment of the invention;

Fig. 5 is a plan view of an alternative control member for mounting within the housing;

Fig. 6 is a plan view showing the reverse side of the control arrangement of Fig. 5; and

Fig. 7 is a partial cross-section detail of the arrangement of Fig. 4 using the control member of Fig. 6.

Detailed Description

[0014] Figs. 1 and 2 depict the housing 11 of the present invention as used to house a DWDM, not shown. Housing 11 comprises a base member 12 and a cap or lid 13. Shown in cutaway in Fig. 2 are a plurality of electrical feed through pins 14 for connecting the components within the housing to an outside temperature control circuit and power source 15 by suitable leads 16. An input buffered or insulated fiber 17 passes into the housing 11 for carrying optical signals to the DWDM within the housing. The signal output of the DWDM circuit exits housing 11 in a fiber ribbon 18, wherein each fiber in the ribbon carries signals of one specific frequency. The ribbon 18 passes into a transition piece 19 wherein each fiber in the ribbon 18 is broken out and exits piece 19 in the form of a single insulated fiber 21, each having a terminating jack plug 22 at its end. In operation, input 17 carries a combination of signals having different wavelengths, and the DWDM circuit within the housing separates the signals by wavelength and applies them to the individual output fibers in ribbon 18. Thus, each of the fibers 21 carries signals of a different wavelength from any of the other fibers. As discussed in the foregoing, these wavelengths are quite closely spaced. For example, in an eight channel ar-

angement, the wavelengths may be 1549.4 nm, 1551.0 nm, 1552.6 nm, 1554.2, 1555.9 nm, 1557.5 nm, 1559.1 nm, and 1560.7 nm. It can be seen that the successive wavelengths increase by only 1.6 nm approximately, a very small incremented difference. Spacings in 32 and 64 channel DWDMs are as small as 0.4 nm.

[0015] The arrangement shown in Figs. 1 and 2 is reversible, *i.e.*, individual fibers 21 may each contain signals each at one of the wavelengths, and the several signals are input to transition piece 19 where the individual fibers are combined in ribbon 18 and their signals are input to the DWDM in housing 11. The DWDM combines the several signals and outputs them on fiber 17.

[0016] Fig. 3 is an exploded perspective view of the housing 11 and the components contained therein. In Fig. 3, the parts discussed in connection with Figs. 1 and 2 bear the same reference numerals as in Figs. 1 and 2 to maintain consistency and avoid confusion.

[0017] Base 12 and cap 13 have at each end channeled aperture members 23 and 24 which are configured to receive, in aperture 23, a fiber collar 26, preferably of thermoplastic rubber, and in aperture 24, a ribbon collar 27, also preferably, of thermoplastic rubber. In assembly, collars 26 and 27 are affixed to fiber 17 and ribbon 18, respectively, and wedged into the channels of aperture 23 and 24 respectively when lid 13 and base 12 are snapped together. These collars 26 and 27 are useful in preventing undue stress on the interior components of the housing, as will be discussed more fully hereinafter. Base member 12 has a projecting rib 28 extending around the periphery thereof which mates with the lower edge 29 of cap member 13. In addition, cap member 13 has projecting members 31 which fit within slots 32 in base 12. This arrangement of ribs, projections, and slots allows cap member 13 to be snapped onto base 12 at assembly, where it is held firmly in place. Base member 12 also has, within its interior, a pedestal member 33, having apertures 34 therein for receiving and holding the several pin members 14, which project through base member 12 to the exterior, as seen in Figs. 1 and 2.

[0018] One or two layers 36 of thermal insulation, such as polyimide fibrous material are carried with base member 12, resting on the floor 37 thereof. The thermal insulation layer 36 has first and second cutouts 38 for receiving first and second support members 39 and 41 which may be a hardened polyimide. On top of each support member 34 and 41 is a double-sided tape member 42 and 43 which is used to hold in place a thermal bed 44. Thermal bed 44 comprises a U-shaped member of suitable thermally conductive material such as aluminum defining a slot 46 which contains a thermally conductive grease 47. Grease 47 may be a highly thermally conductive, silver filled silicone grease. It should maintain its properties over broad environmental and temperature ranges, should be resistant to moisture, humidity, ozone, radiation and many chemicals. Such a material is commercially available as Norsil 56840-EC, pro-

duced by Norlabs Co.

[0019] Carried within the slot 47 is, for example, an OASIC circuit die 48 for a DWDM, as discussed hereinbefore. While thermal bed 44 is shown as U-shaped, it is possible that other shapes or configurations might be used, so long as it is capable of substantially covering circuit 48, and is capable of retaining the grease within the slot. One leg 49 of circuit 48 is connected to fiber 17 by means of a connector 51 and the other leg 52 is connected to ribbon 18 by means of a connector 52. Connectors 51 and 52 are preferably of the type shown and described in U.S. patent 5,559,915 of Deveau, and the connections made using the techniques shown and described in that patent. On the top surface 53 of thermal bed 44 and affixed thereto is a heater element 54, preferably of the resistive type, which is connected to pins 14 by leads 56, shown in dashed lines. Heater element 54 may also be wrapped around and contacting the bottom surface. Also affixed to surface 53 are first and second resistance temperature devices 57 and 58, also connected to the pins 14 by leads 59 and 61, shown in dashed lines. Overlying the thermal bed 44 and the associated components is a thermal insulating blanket 62, which fits into the interior of cap or lid 13.

[0020] When the housing 11 and the various components are assembled, pins 14 are inserted into holes 34 and the various leads 56, 59, and 61 are connected thereto, thereby connecting those leads to the various leads 16 and the control device 15. Although the housing 11, *i.e.*, base 12 and lid 13 are made of a material having a low thermal coefficient of expansion (TCE), such as glass filled polyphenylene sulfide (PPS), the TCE is still greater than that of the silicon substrate of the wafer 48, and such differential can lead to stress, especially on the waveguide (DWDM) to fiber joints. In order that such stress be minimized, the housing is heated during assembly to, for example, at least 85°C, and the ribbon and fiber collars 26 and 27, which are exposed to the fiber 17 and ribbon 18 respectively, wedged in place in their respective channeled apertures 23 and 24. After the epoxy cures, the package device is cooled to ambient temperature. Inasmuch as the PPS material shrinks only slightly on cooling, the fiber-waveguide joints remain under compression and thus the stress thereon is minimized for any temperatures below 85°C, which includes the operating temperature range of 75°C to 90°C. Also, in preparation for operation, the heating element 54 heats the thermal bed 44 up to the operating range of 75°C to 90°F, or, more particularly to a predetermined temperature dependent on the particular circuit, within the operating range. Inasmuch as the DWDM wafer 48 floats in the thermal conducting grease 47 within the slot 46, it is not subject to stresses due to the expansion of thermal bed 44. Furthermore, since as collars 26 and 27 are locked in place in their apertures 23 and 24, external stresses on fiber 17 and ribbon 18 are not communicated to the interior of the housing.

[0021] In operation, the resistance temperature devic-

es 57 and 58, which have been previously calibrated, indicate to the control unit 15 any temperature variation of the thermal bed 44, and heating element 54 is activated to apply heat thereto, thus maintaining the desired operating range of 75°C to 90°C. The entire housing 11 thus provides a virtually stress free environment for the DWDM or other OASIC type wafer contained therein.

[0022] The embodiment of the invention shown in Figs. 1 through 3 includes a control circuit 15 external of the housing 11 and connected to the interior components thereof through leads 16 and pins 14. In Figs. 4 through 7 there is shown an alternative embodiment of the invention in which the control circuit is contained within the housing 11. For simplicity, parts corresponding to like parts in Figs. 1 through 3 bear the same reference numerals.

[0023] As shown in Fig. 4, base member 12 is slightly modified from base member 12 of Fig. 3. Thus, the end edges 71 and 72 of base 12 are channeled to receive stress relieving members 73 and 74 through which the fiber 17 and ribbon 18 respectively pair. Members 73 and 74 may be made of thermoplastic rubber, for example. In addition, base member 12 has, centrally located along one side 76, a porch member 77 adapted to mate with a similar porch member 78 on cap or lid 13. As will be apparent hereinafter, when the cap 13 is snapped on to base 12, porches 77 and 78 form a slot through which a ribbon conductor 79 may pass to the exterior.

[0024] Ribbon conductor 79, as is apparent with reference to Figs. 5 and 6 has a control circuit 91 formed thereon for responding to signals from the resistive temperature device 57 to activate heater 54. As can be seen in Figs. 5 and 6, ribbon conductor 79, which is formed from a flexible insulating material which contains a plurality of individual conductors 82, connects control circuit 81 to resistive temperature devices 57 and 58. In addition, control circuit 81 is connected to heater 54 and to a second heater 83 by means of conductive leads 84, and 86 in a manner such that heaters 54 and 83 are connected in series. Ribbon conductor 79 has a plurality of solder connection points 87 for connecting the conductors 82 to the pins 14 shown in Fig. 3, if desired, in which case the ribbon 79 may be cut just below the solder points 87. On the other hand, the pins 14 may be eliminated and the ribbon 79 fed through the slot 88. Slot porches 77 and 78 may both contain a thermal insulating member 89 for holding the ribbon 79 in place within slot 88, and for thermally insulating the interior of housing 70.

[0025] Fig. 7 is a cross-sectional view of the housing 70 showing the manner by which thermal bed 44 and control circuit 81 are placed within housing 70. On the floor of base member 12 is a double-sided tape 91, for example, although other securing means might be used, over which ribbon 79 passes and is held in place. Ribbon 79 is looped upward at 92 so that the thermal bed 44 overlies the control circuit 81 and is separated therefrom by insulating member 36, or by other suitable insulating

means. The insulation 36 protects the control circuit from the heat of thermal bed 44 and electrically from the metal thereof.

[0026] It can readily be seen that the embodiment of Figs. 4 through 7 is basically similar to that of Fig. 3 with only slight modifications thereto to permit placing the control circuit 81 within the housing 70. The electrical connections to the exterior are for a power supply and for affording testing and calibrating operations to be performed. The actual temperature control, however, is within the housing 70.

[0027] The housing of the invention constitutes a means of protecting various types of printed circuits and the like from the deleterious consequences of temperature changes, by insuring a stable temperature environment for such circuits. Various modifications or adaptations of the principles of the invention to specific specialized uses may be made which, while perhaps involving different physical configurations, do not depart from those principles as disclosed in the foregoing.

[0028] In concluding the detailed description, it should be noted that it will be obvious to those skilled in the art that many variations and modifications may be made to the preferred embodiment without substantially departing from the principles of the present invention. All such variations and modifications are intended to be included herein as within the scope of the present invention as set forth in the claims. Further, in the claims hereafter, the corresponding structures, materials, acts, and equivalents of all means or step plus function elements are intended to include any structure, material, or acts for performing the functions with other elements as specifically claimed.

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Claims

1. A housing for an optical integrated circuit comprising:

40 a base member;
a thermal bed supported within said base member, said thermal bed having first and second legs spaced from each other defining a slit for holding an optical integrated circuit;
45 thermally conductive means within said slit for holding the optical integrated circuit in suspension within said slit;
heater means for heating said thermal bed mounted on one of said legs; and
50 a cap member for combining with said base member to produce a closed housing.

2. A housing as claimed in claim 1 wherein said heater means is mounted on both of said first and second legs, or the thermal bed comprises a substantially U-shaped member of thermally conductive material.

3. A housing as claimed in claim 1 wherein said material is aluminum, or the thermally conductive means comprises a silvered silicone grease, or a temperature monitoring device is formed on one of said legs.

4. A housing as claimed in claim 3, and further comprising a control circuit for receiving temperature indications from said temperature monitoring device, and activating said heater means to achieve a specified temperature within the range of 75°C to 90°C, the control circuit either being external of the housing and electrically connected to said monitoring device and said heater means, or contained within said housing and is electrically connected to said monitoring device and said heater means.

5. A thermal housing for an optical component for maintaining said component at a temperature within a predetermined temperature range, said housing comprising:

a base member;
 a thermal bed member;
 means for thermally insulating said thermal bed from said base member and for supporting said thermal bed member therein;
 said thermal bed member having first and second legs spaced from each other defining a slit for holding an optical component;
 thermally conductive means within said slit for holding an optical component in suspension within said slit;
 heater means for heating said thermal bed in contact with said thermal bed means;
 temperature sensitive means for monitoring the temperature of said thermal bed and generating electrical signals indicative thereof;
 a control circuit for receiving signals from said temperature sensitive means and actuating said heater means to maintain the temperature of said thermal bed within said predetermined temperature range; and
 a cap member adapted to mate with said base member.

6. A thermal housing as claimed in claim 5, wherein said temperature range is 75°C to 90°C.

7. A thermal housing as claimed in claim 5, wherein the optical component has first and second optical fiber members connected thereto and said housing has channeled apertures through which the optical fiber members pass, or the base member has a plurality of feed through pins for electrically connecting the interior of said housing to the exterior thereof.

8. A thermal housing as claimed in claim 7, wherein

5 said control circuit is either exterior of said housing and is connected to said temperature sensitive means and said heater means by means of said feed through pairs, or is contained within said housing, or is formed on a ribbon conductor, and is connected to said heater means and said temperature sensitive means thereby, said ribbon conductor optionally extending from said control circuit through a slit in said housing to the exterior thereof.

10 9. A method for reducing thermally induced stress in optical fiber/circuit connections within a housing and the optical fibers leading into and out of the housing, through apertures therein, comprising

15 heating the housing to induce expansion thereof;
 affixing collars to the optical fibers;
 wedging the collars in the apertures in the housing; and
 cooling the housing to ambient temperature.

20 10. The method as claimed in claim 9, wherein the collars are affixed to the optical fibers by epoxy, or the step of curing the epoxy prior is achieved to cooling the housing, or the housing is heated to at least 85°C.

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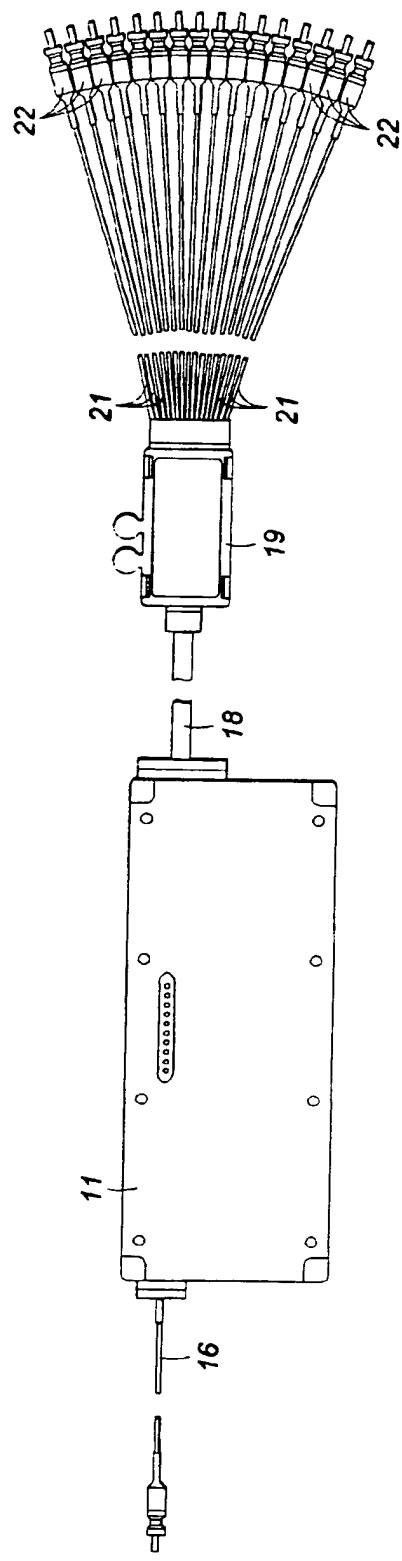


FIG. 1

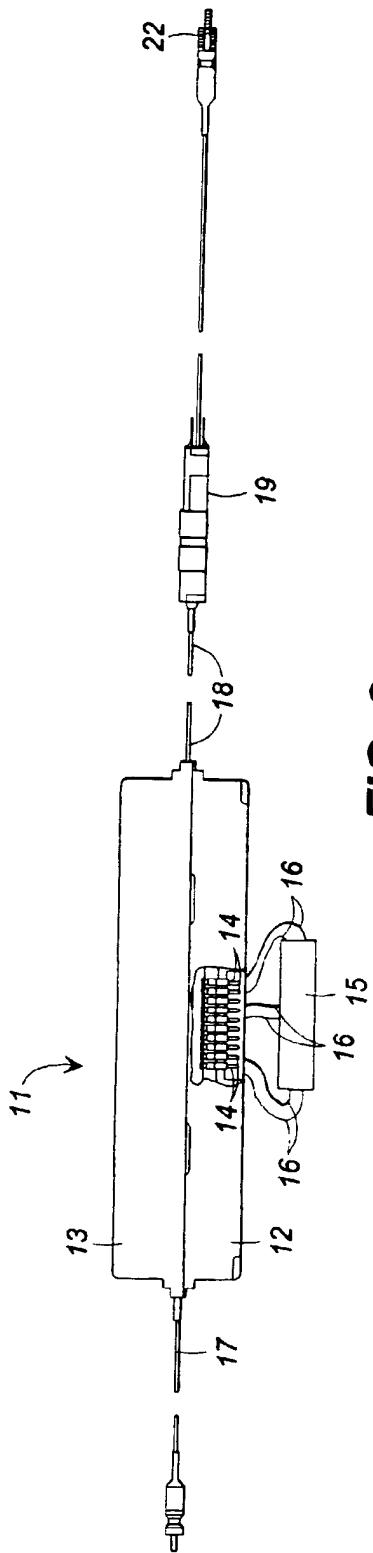


FIG. 2

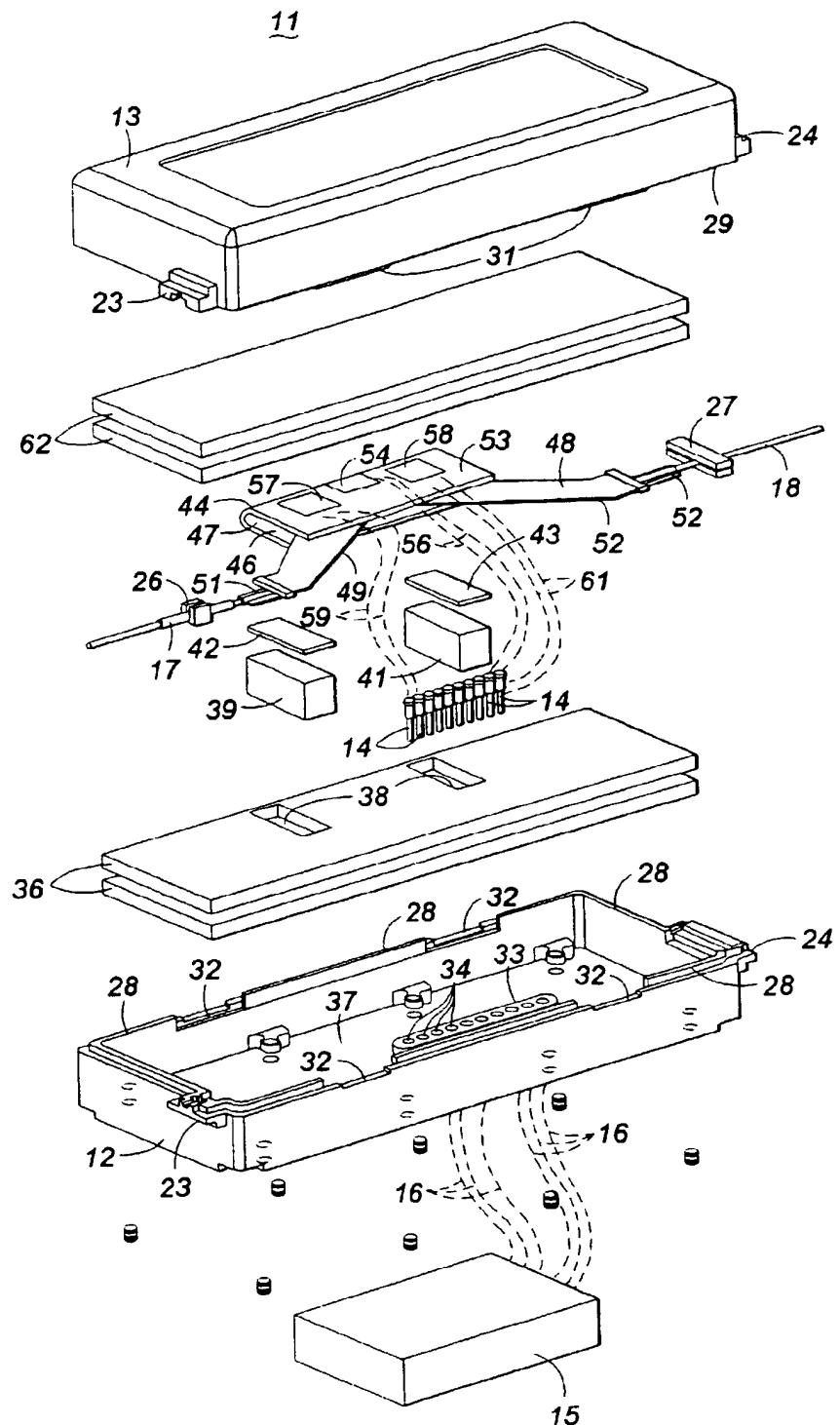


FIG. 3

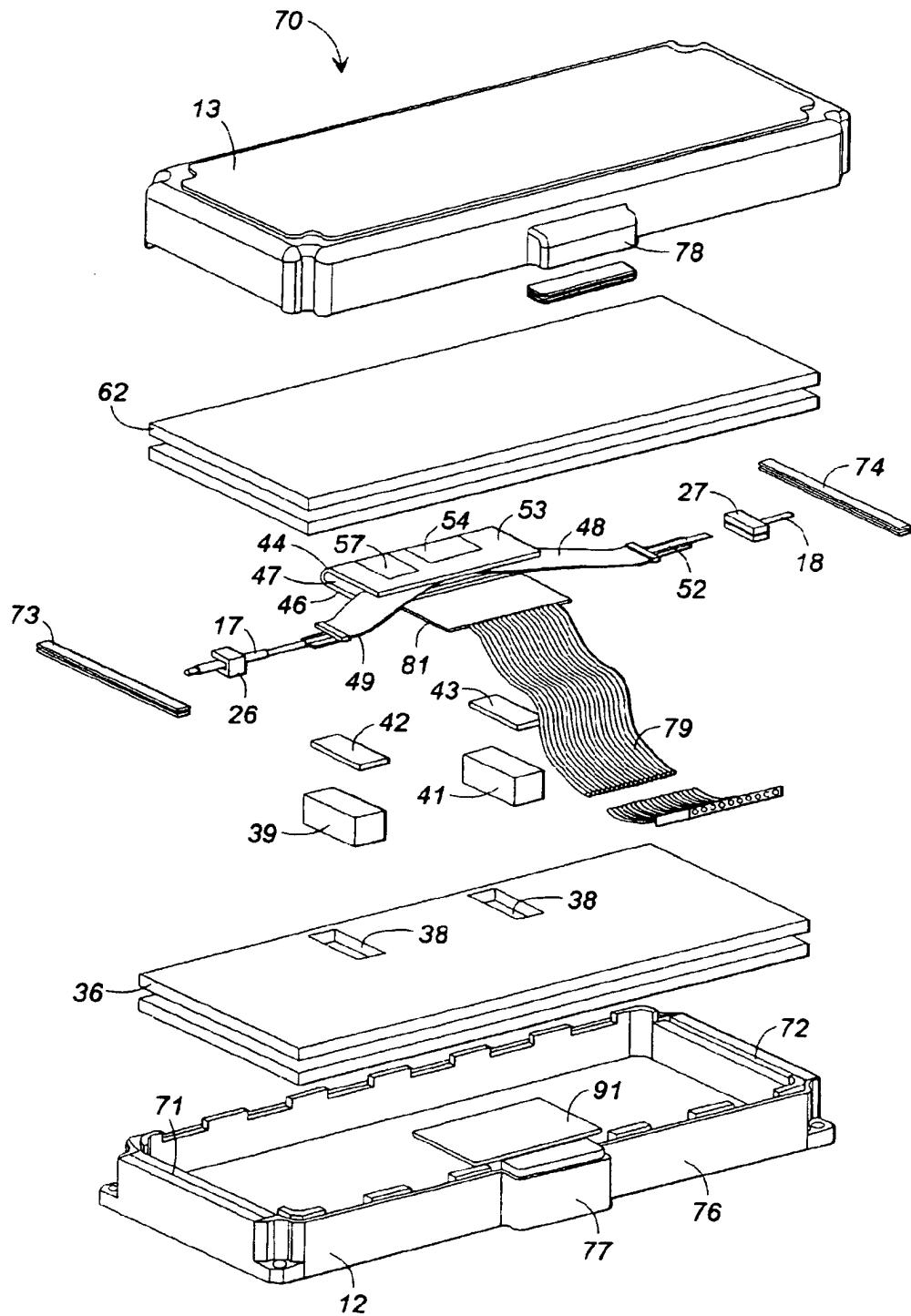


FIG. 4

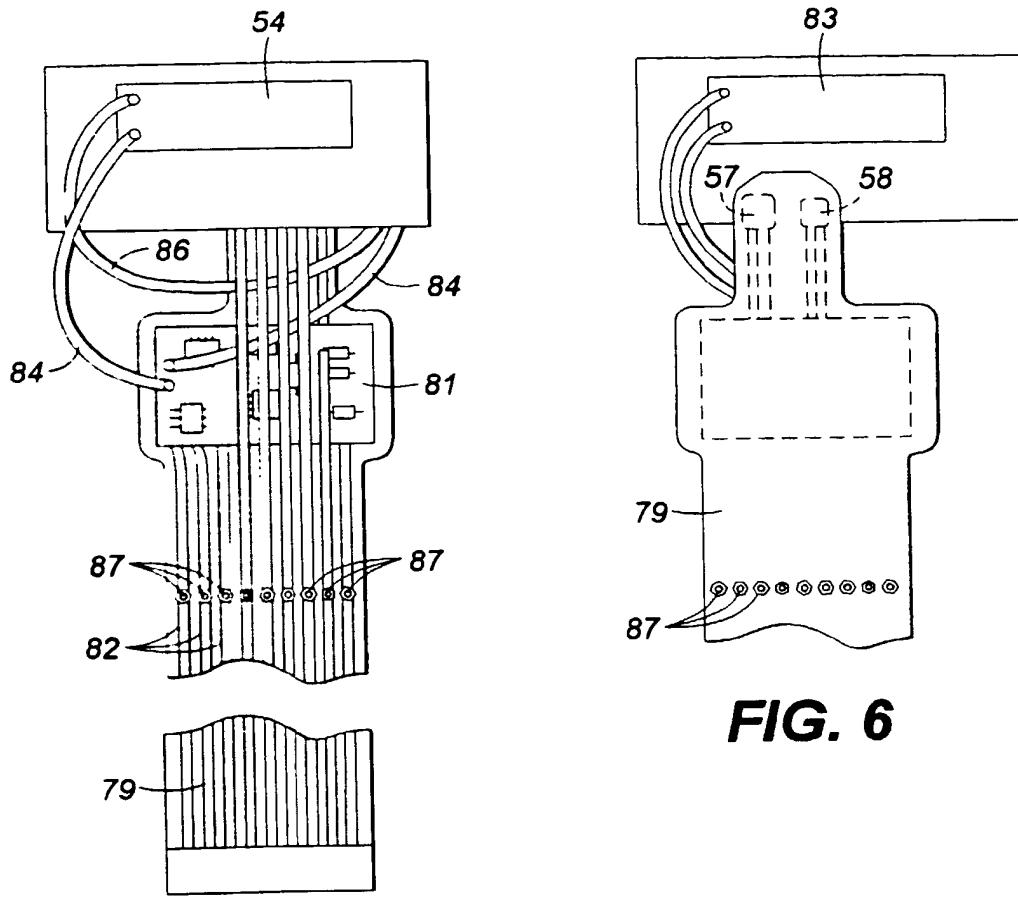


FIG. 5

FIG. 6

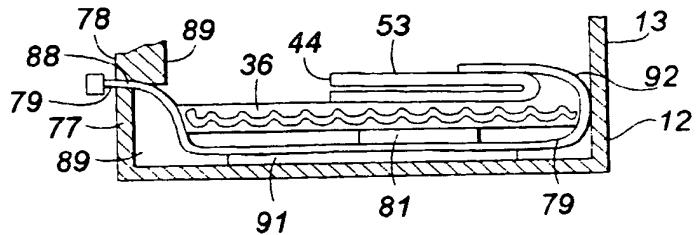


FIG. 7



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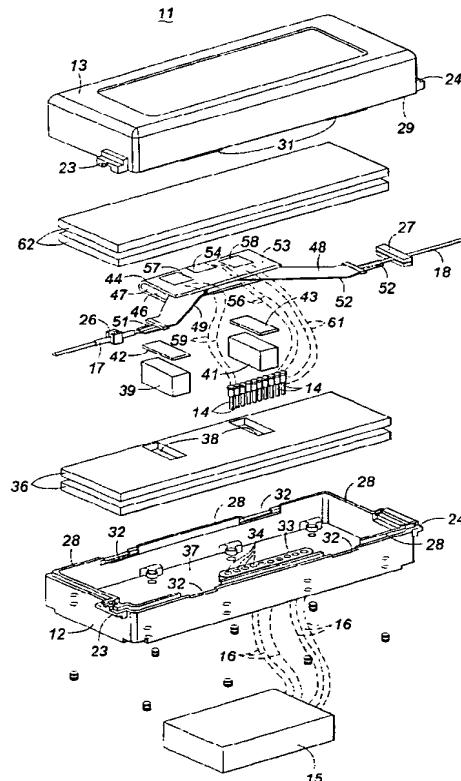


FIG. 3



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 98 31 0065

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	PATENT ABSTRACTS OF JAPAN vol. 097, no. 011, 28 November 1997 (1997-11-28) & JP 09 179078 A (TERA TEC:KK), 11 July 1997 (1997-07-11) * the whole document ---	1,4,5	H05K1/02 H05K7/20 G02B6/12 H01L23/38 H01L23/36 H01S3/043 G05D23/00 G02B6/30 G02B6/42
A,D	US 4 968 121 A (BRUSSELBACH HANS W ET AL) 6 November 1990 (1990-11-06) * column 4, line 37 - line 68 * * column 5 - column 7 * * column 8, line 1 - line 64 * * figures 1-3 * ---	1,5	
A	US 3 883 715 A (GEBO CHARLES HENRY) 13 May 1975 (1975-05-13) * column 3, line 8 - line 68; claims; figures 1-4 * * column 4, line 1 - line 41 * ---	1,4,5	
A	US 5 539 618 A (WIESA THOMAS ET AL) 23 July 1996 (1996-07-23) * column 2, line 60 - line 67 * * column 3, line 1 - line 62 * * figure 1 * ---	1,5	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H05K G02B H01L H01S G05D
A	WO 94 11763 A (WESTERN ATLAS INT INC) 26 May 1994 (1994-05-26) * claims; figures * ---	1,4,5	
A	US 5 646 826 A (KATCHMAR ROMAN) 8 July 1997 (1997-07-08) * column 5, line 32 - line 67 * * column 6, line 1 - line 65 * * column 9, line 1 - line 67 * * column 10, line 1 - line 40 * * figures 1-3,11-16 * ---	1,5	
		-/-	
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
THE HAGUE		21 December 2000	MATHYSSEK K.
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone	T : theory or principle underlying the invention		
Y : particularly relevant if combined with another document of the same category	E : earlier patent document, but published on, or after the filing date		
A : technological background	D : document cited in the application		
O : non-written disclosure	L : document cited for other reasons		
P : intermediate document	& : member of the same patent family, corresponding document		



European Patent
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Application Number
EP 98 31 0065

CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):

- No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

- All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

- As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.

- Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:

- None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



European Patent
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EUROPEAN SEARCH REPORT

Application Number
EP 98 31 0065

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	PATENT ABSTRACTS OF JAPAN vol. 018, no. 661 (E-1644), 14 December 1994 (1994-12-14) & JP 06 260722 A (CANON INC), 16 September 1994 (1994-09-16) * abstract * <p>---</p>	1,4,5	
A	INOUE Y ET AL: "ATHERMAL SILICA-BASED ARRAYED-WAVEGUIDE GRATING (AWG) MULTIPLEXER" IOOC-ECOC. EUROPEAN CONFERENCE ON OPTICAL COMMUNICATION ECOC INTERNATIONAL CONFERENCE ON INTEGRATED OPTICS AND OPTICAL FIBRE COMMUNICATION IOOC, no. 448, 22 September 1997 (1997-09-22), pages 33-36, XP002058688 * the whole document * <p>---</p>	1,5,7	
A	EP 0 795 766 A (PIRELLI CAVI SPA) 17 September 1997 (1997-09-17) * column 5, line 15 - line 44 * * column 10, line 32 - line 59 * * column 11, line 1 - line 45 * * figures 1-5 * <p>---</p>	9	TECHNICAL FIELDS SEARCHED (Int.Cl.6)
A	US 5 664 043 A (DONALDSON ALAN ET AL) 2 September 1997 (1997-09-02) * column 3, line 14 - line 67 * * column 4 - column 5 * * column 6, line 1 - line 17 * * figures * <p>-----</p>	9	
<p>The present search report has been drawn up for all claims</p>			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	21 December 2000	MATHYSSEK K.	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention. E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			



European Patent
Office

LACK OF UNITY OF INVENTION
SHEET B

Application Number
EP 98 31 0065

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-8

refers to a housing for a circuit or component comprising a base member, thermal bed having legs defining a slit, thermally conductive means within the slit for holding the circuit or component in suspension within the slit, heater means and a cap member

2. Claims: 9,10

is directed to a method for reducing thermally induced stress in optical fibre/circuit connections within a housing and optical fibres leading into and out of the housing through apertures

ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

21-12-2000

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